

ABSTRACT OF THE DISCLOSURE

A pseudo-random number generator comprises a linear feedback register for generating pseudo-random numbers; and a signal generator for generating a shift clock for operating a linear feedback register and
5 predetermined input data. The linear feedback register has a plurality of registers connected in series, a first logical operation circuit for taking logical operation of output data from predetermined registers to deliver the result thereof, and a second logical operation circuit for taking logical operation of
10 input data supplied from the outside and output data of the first logical operation circuit to supply the result thereof to any one of the registers.